## MINISTRY OF COMMUNICATIONS AND INFORMATION TECHNOLOGY

### (Department of Electronics and Information Technology)

#### NOTIFICATION

No. 24(10)/2010-IPHW

New Delhi, the 27<sup>th</sup> July 2012

Subject: Modified Special Incentive Package Scheme (M - SIPS) to offset disability and attract investments in Electronics Systems Design and Manufacturing (ESDM) Industries

#### Background and Objective

- Manufacturing base of electronic products in the country is grossly 1.1 inadequate in comparison to demand of such goods. Even in cases where products are manufactured in India, the extent of domestic value addition is low. Semiconductors are at the heart of any electronic systems and constitute a significant part of the total value of the Bill of Material (BOM). The main impediments in the way of attracting investments for manufacture of semiconductors, components and electronic products are disabilities like high cost of power and finance, high transactional costs and poor base of supply chain (components and accessories). Presently, semiconductor wafers, semiconductors and majority of the hi-tech components are being imported. The manufacturing capabilities for semiconductors are highly capital intensive and have to deal with constantly changing technology. Development of supply chain is essential for the manufacture of electronic products with higher domestic value addition. Therefore, Government has decided to offer a package of incentives to attract domestic and global investments into the Electronics Systems Design and Manufacturing (ESDM) sector within Electronics Manufacturing Clusters (EMCs) as a means to minimizing the disabilities.
- 1.2 As per the report of the Task Force on IT/ITES and Electronics Hardware Manufacturing, the ESDM industry in India lags behind its counterparts in other countries despite having basic capabilities to develop the

sector due to its strength in chip design and embedded software. The proposed incentives will provide a level-playing field and achieve competitiveness in the global market.

1.3 ESDM industry in India faces significant challenges like high transactional costs, complex administrative processes and infrastructural deficiencies. An analysis by the Federation of Indian Export Organization (FIEO) reveals that cost disabilities, including the transaction costs borne by Indian exporters vary and range from 19 to 22%. In contrast exporters in developed economies pay only 2 to 3% of transactional costs. The Modified SIPS proposes to partly offset the difficulties.

## 2. Modified Special Incentive Package:

- 2.1 The M-SIPS will be applicable to investments in new ESDM units and expansion of capacity/ modernization and diversification of existing ESDM units. For the purpose of the scheme -
- a) ESDM unit shall mean a unit engaged in design and manufacturing of the electronics and nano-electronics products and their accessories listed in Annexure 2 and includes all stages of the value-chain involved in the development of these products and accessories starting from raw materials. It includes assembly, testing and packaging. It also includes Electronics Manufacturing Services (EMS) involved in any of the above stages.
- b) Expansion of an existing unit would mean increase in the value of fixed capital investment in plant and machinery of an ESDM unit by not less than 25%, for the purposes of expansion of capacity/ modernization and diversification.
- 2.2 The Modified Special Incentive Package shall be for state-of-the-art technology.
- 2.3 The investment thresholds applicable for units in different categories under the scheme are indicated in **Annexure 1**. The thresholds are the same

for new or expansion of capacity/ modernization and diversification of existing units. The incentives available for each category are also indicated along with in **Annexure - 1**.

- 3.1 The incentives mentioned in **Annexure 1** will be available for investment made within 10 years of the approval of a new unit. In case of expansion of capacity/ modernization and diversification of existing units, the incentive will be available for investment made within 10 years of the approval of expansion of capacity/ modernization and diversification of existing units
- 3.2 The incentives, if any, offered by the State Government or any of its agencies or local bodies shall be over and above the incentive amount eligible under M-SIPS.
- 3.3 The period of 10 years shall be the first 10 years after the approval of the project and not with regard to any subsequent phase/s of the project.
- 3.4 The capital expenditure will be the total of capital expenditure in land, building, plant and machinery and technology including Research and Development (R&D). The total cost of land exceeding 2% of the capital expenditure shall not be considered for calculation of incentives in this regard.
- 4.1 The incentive against the capital expenditure shall be released after the end of the financial year in which the total investment exceeds the threshold value. Thereafter, the incentive shall be provided on an annual basis on the value of the investments made during the year and be restricted to the first 10 years from the date of approval of the unit under the scheme.
- 4.2 The re-imbursement of Central taxes and duties actually paid, where applicable, shall be released after the end of the financial year in which the unit commences the production. Thereafter, the incentive shall be provided on an annual basis on the value of the taxes and duties actually paid during the year and will be restricted to the first 10 years from the date of approval of the unit.

- 5.1.1 The overall financial ceiling under the Modified SIPS will initially be limited to Rs 10,000 crores during the 12<sup>th</sup> Five Year Plan. A review of the scheme in the ultimate year of the 12<sup>th</sup> Five Year Plan could make a realistic estimate of the likely demand for the 13<sup>th</sup> Five Year Plan.
- 5.1.2 Periodic reviews will be undertaken with respect to changes, if any, to the list of products and thresholds for which incentives are provided.
- 5.2 The M-SIPS will be open for applications for three years from the date of this notification. The applications received under the scheme will be appraised on an ongoing basis.
- 5.3 The M-SIPS will be available for units set up within Electronics Manufacturing Clusters to be notified by the Department of Electronics and Information Technology.
- 6.1 An M-SIPS Appraisal Committee shall be set up by the Department of Electronics and Information Technology and headed by the Additional Secretary, Department of Electronics and Information Technology or in his absence on long leave or the post being vacant any other officer in the Department not below the rank of Additional Secretary, who may be so authorized by the Minister incharge. The Appraisal Committee will consider the applications under M-SIPS and submit its recommendations to the Government. The Government shall consider such recommendations and grant approvals.
- 6.2 For effective functioning of the M-SIPS Appraisal Committee, a set of guidelines shall be drawn up by Department of Electronics and Information Technology and issued separately.

Dr. Ajay Kumar)

Joint Secretary to Government of India

Annexure 1
Investment Thresholds and Financial Incentives for ESDM

S.	Type	Inv	estment Thr (in Rs. Cror		Financial Incentives		
NO.		Fab	Assembly, Testing, Marking & Packaging (ATMP)	Manufa- cturing	SEZ	Non-SEZ	
1.	Electronic Products including Nano- Electronic and Telecom Products as per Annexure 2	-	-	10	20% of Capex	25% of Capex + Reimbursement of Excise/CVD on capital equipment	
2.	Intermediates						
2.1	Nano Electronic Components	-	-	200	20% of Capex	25% of Capex + Reimbursement of Excise/CVD on capital equipment	
2.2	Semiconductor Wafering			1000	20% of Capex + Reimburs ement of Central Taxes and Duties (See Note - 1)	25% of Capex + Reimbursement of Excise/CVD on capital equipment + Reimbursement of Central Taxes and Duties (See Note - 1)	
2.3	Semiconductor (	Chips (Se	e Note 2):				
a)	Logic – Microprocessor	2000 rs,	500	N.A	20% of Capex +	25% of Capex + Reimbursement of	

	Microcont						
	Digital Sig					Reimburs	- WOISELC A D OU
	Processors	s (DSP)			е	ment of	capital equipmen
	& Applicati				C	entral	+ Reimbursemer
	Specific				Ta	axes and	of Central Taxes
	Integrated				Du	uties	and Duties (See
	Circuits (AS	SICS)			(S	ee Note	Note - 1)
	,	/.			- 1	)	.,
b)	Memory	1	5000 400				
			400	N.A	20%	% of	25% of Capex +
					Cap	pex +	Reimbursement of
					1	mburs	Excise/CVD on
						ent of	
					Cen		capital equipment + Reimbursement
					Taxe	es and	of Central Taxes
					dutie		
					(See		and Duties (See Note - 1)
2.3	Chip Compone	nto D:					11016 - 1)
a)	Chip Compone Chip	nis, Disc	crete Semicono	ductors and Po	wer Sem	iconduct	tore:
		200	75	N.A	20% c		
	Components				Capex	-	25% of Capex +
					Capex	5.6	Reimbursement of
						- 1	xcise/CVD on
2) (	Diagnost	25	10	N.A		Cá	apital equipment
	Discrete			14.7	20% of	20	70/
8	Semiconductors			11.2	20% of	25	% of Capex +
S	Semiconductors ke Transistors,			1	20% of Capex	Re	5% of Capex +
S	Semiconductors			N.A		Re Ex	5% of Capex + eimbursement of ccise/CVD on
S li	Semiconductors ke Transistors, Diodes etc.			N.A		Re Ex	5% of Capex +
S li	Semiconductors ke Transistors, liodes etc.		50		Capex	Re Ex ca	5% of Capex + eimbursement of cise/CVD on pital equipment
Se Se	Semiconductors ke Transistors, liodes etc. ower emiconductors		50	N.A	Capex 20% of	25 Re Ex cap	5% of Capex + eimbursement of cise/CVD on pital equipment
li D Per Se (in	Semiconductors ke Transistors, Diodes etc.  ower emiconductors		50		Capex	Rei Ex cap	5% of Capex + eimbursement of ccise/CVD on pital equipment % of Capex + mbursement of
) Po Se (in	Semiconductors ke Transistors, liodes etc. ower emiconductors acluding ffusion) like		50		Capex 20% of	25% Rei Ex caj Rei Exc	5% of Capex + eimbursement of cise/CVD on pital equipment % of Capex + mbursement of ise/CVD on
) Po Se (in Diff	Semiconductors ke Transistors, liodes etc. ower emiconductors acluding ffusion) like		50		Capex 20% of	25% Rei Ex caj Rei Exc	5% of Capex + eimbursement of ccise/CVD on pital equipment % of Capex + mbursement of
) Po Se (in Diff FE MC	Semiconductors ike Transistors, liodes etc.  ower emiconductors icluding iffusion) like Ts, OSFETs,		50		Capex 20% of	25% Rei Ex caj Rei Exc	5% of Capex + eimbursement of cise/CVD on pital equipment % of Capex + mbursement of ise/CVD on
) Po Se (in Diff FE MC SCI	Semiconductors ke Transistors, liodes etc.  ower emiconductors icluding ffusion) like Ts, OSFETs, Rs, GTDs,		50		Capex 20% of	25% Rei Ex caj Rei Exc	5% of Capex + eimbursement of cise/CVD on pital equipment % of Capex + mbursement of ise/CVD on
) Po Se (in Diff FE MC SCI	Semiconductors ike Transistors, liodes etc.  ower emiconductors icluding iffusion) like Ts, OSFETs,		50		Capex 20% of	25% Rei Ex caj Rei Exc	5% of Capex + eimbursement of cise/CVD on pital equipment % of Capex + mbursement of ise/CVD on
) Po Se (in Diff FE MC SCI	Semiconductors ke Transistors, liodes etc.  ower emiconductors icluding ffusion) like Ts, OSFETs, Rs, GTDs,		50		Capex 20% of	25% Rei Ex caj Rei Exc	5% of Capex + eimbursement of cise/CVD on pital equipment % of Capex + mbursement of ise/CVD on

2.4	Solar Photovoltaics (SPV) (See Note - 3):										
a)	Polysilicon Technology:										
i)	Polysilicon	650	N.A	N.A	20% of Capex	25% of Capex + Reimbursement of Excise/CVD on capital equipment					
ii)	Ingots and/ or wafers	400	N.A	N.A	20% of Capex	25% of Capex + Reimbursement of Excise/CVD on capital equipment					
iii)	Cells or Cells & Modules	100	N.A	N.A	20% of Capex	25% of Capex + Reimbursement of Excise/CVD on capital equipment (See Note – 3)					
b)	Thin Film Techno	logy									
i)	Cells or Cells & Modules	300	N.A	N.A	20% of Capex	25% of Capex + Reimbursement of Excise/CVD on capital equipment					
2.5	Light Emitting Diodes (LED)	250	50	N.A	20% of Capex	25% of Capex + Reimbursement of Excise/CVD on capital equipment					
2.6	Liquid Crystal Dis	plays (L	CD):								
a)	LCD Fabrication	4000	250	N.A	20% of Capex	25% of Capex + Reimbursement of Excise/CVD on capital equipment +Reimbursement of Central Taxes and Duties (See Note - 1)					
b)	LCD Glass Substrate	250	100	N.A	20% of Capex	25% of Capex + Reimbursement of Excise/CVD on					

	2.7.	Passive C	ompone	nts. Ele	ectro - N	100h==					capital equipme
		Consumab	les & A	cesso	ries.	rechani	cal Co	mpon	ents, N	1echa	capital equipme
	a)	Passive		N.A		.A					
		Componen	ts	0.0000000000000000000000000000000000000	I IN	. A		5	20%	of	25% of Capex +
		such as							Сар	ex	Reimbursement
		Resistors,									Excise/CVD on
		Capacitors,									capital equipment
		Inductors,									oquipmem
		Thermistors	etc.								
k	_	Electromech		N.A	NI A						
	- 1	al Componen		1.7	N.A		5		20%	of	25% of Capex +
		and Mechanic							Capex	<	Reimbursement of
		arts such as									Excise/CVD on
		lultilayer PCI									capital equipment
		ransformers,									equipment
		oils,									
	Co	onnectors,									
		vitches,									
	212	rrites, Micro									
		tors, Steppe	r								
		tors, Films									
	etc.										
2)	Con	sumables									1
70	1	Accessories	N.A		N.A		1	20	% of	24	50/ - 1.0
		as Mobile						1	pex	20	% of Capex +
	1	nes and IT							POX		eimbursement of
		ssories -								1	cise/CVD on
	Batte									Ca	pital equipment
		gers etc.,									
		Foils,									
		s, Epoxy,									
		ets etc.									
E	lectr	onic	N.A	N.	Δ	- 12					
N	lanut	facturing		14.		100		20%		25%	of Capex +
-		The second second			1			Cape		_	bursement of

 Services	Excise/CVD on
(EMS) – (See	capital equipment
Note – 4)	

Note - 1: Central Taxes and Duties mean Customs Duty, Service Tax, Excise Duty, on actual from the date of commencement of production.

<u>Note -2</u>: The types of fabs for which incentives are available for setting up of Semiconductor Wafer Fabrication facilities (Fabs) will be decided based on the outcome of investment being attracted by the Government to set up two Fabs in a separate exercise.

<u>Note -3</u>: Incentives for investments in additional Solar Photovoltaics shall be decided based on the outcome of proposals received under the Special Incentive package Scheme (SIPS) announced vide Notification No. 3 (1)/ 2007 – IPHW (SIPS) dated 21 March 2007.

<u>Note – 4:</u> Electronic Manufacturing Services (EMS) would mean, units engaged in providing services related to manufacture of sub-assemblies and parts including integration services to the Original Equipment Manufacturers (OEMs). However, EMS shall not encompass production of final products under their own brand name.

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# List of verticals of ESDM for which incentives are available under M-SIPS Annexure 2

	(A)	Electronics Productives are available under M-SIPS
		Telecom Products (SI no. 1 of Approx
2	ON Proceed on the contract of	Communication Equipment; Satellite Communication Equipment; IP based new generation soft-switches/routers including L2 and L3 switches, data networking equipment — copper/optical — consumer and carrier grades, for public and private networks; Transport systems — DWDM, SDH, PON, Transport Platform (P-OTP); Wireless technology — GSM (2G & 2.5G), Microwave Radio systems 2-70 GHz, Software defined radio, Cognitive arrivelllance, processing of speech, data, image, video; Customer are sequipment (CPE) — PBX systems, eadends, 3G Routers, VoIP DES, Mobile phones/ Mobile handsets/ Smart Mobile phones, Set-top nsors; VSAT based systems — Broadband, Disaster management; Non-nputers; NMS/OSS/BSS systems for all above — MP/Openview/CORBA; Customer care & Billing systems; Electronics within, Video Conferencing Equipment, Advanced storage batteries such as
2	serve	ardware products including computers, (tablets, desktops etc.) ers, peripherals like printers, faxes, storage devices monitors, etc.
3.	etc.	sumer Electronics like Televisions, Digital Cameras, Camcorders
4.	Healtl	n and Medical Electronics
5.	Strate	gic electronics
6.	Solar I	Photo Voltaic including thin film, polysilicon etc.
7.	Light E	mitting Diodes (LEDs)
8.	Liquid	Crystal Displays (LCDs)
9.	Avionic	s

		Industrial Electronic products including measuring and control equipment, energy meters etc.					
	11	. Nano electronic products					
	12	e-waste processing/ recycling  Automotive Electronics like Anti-lock braking system, Electronic Brake  Distribution, Traction Control, etc.					
	13						
	14.						
	15	Energy conservation electronics					
	16.	Opto-electronics					
	17.	Bio-metric and identity devices/ RFID					
,	18.	Power supplies for ESDM products					
(	B)	Intermediates: (SI no. 2 of Annexure 1)					
1		Nano-electronic components					
2		Semiconductor wafering					
3.	1						
4.	+	Semiconductor chips including logic, memory and analog					
5.	-	All Assembly, Testing, Marking and Packaging of ESDM Units					
6.	-	Chip components					
	1	Discrete Semiconductors like Transistors, Diodes					
7.	_	Power semiconductors (including diffusion) like FETs, MOSFETs, SCRs, GTDs, IGBT etc.					
8.	F	Electromechanical Components and Mechanical Parts such as Multilayer CBs, Transformers, Coils, Connectors, Switches, Ferrites, Micro Motors, tepper Motors, Films etc.,					
9.	C	consumables and Accessories such as Mobile Phones and IT cocessories - Batteries, Chargers etc., PCBs, Foils, Tapes, Epoxy, abinets etc.					
10.	A	Fabrication Manufacturing facilities (Fabs) for ESDM products					
	140	ote: Any intermediates not covered above shall be decided and rmitted under the scheme by the Competent Authority.					
<b>.</b>	Ele	ectronics Manufacturing Services (EMS) (SI no. 3 of Annexure 1)					

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